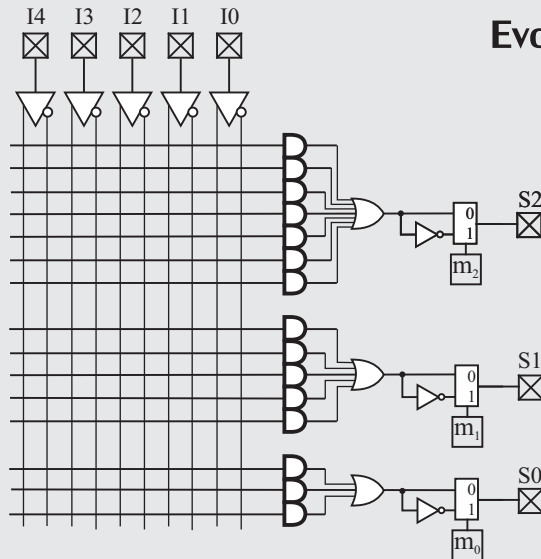
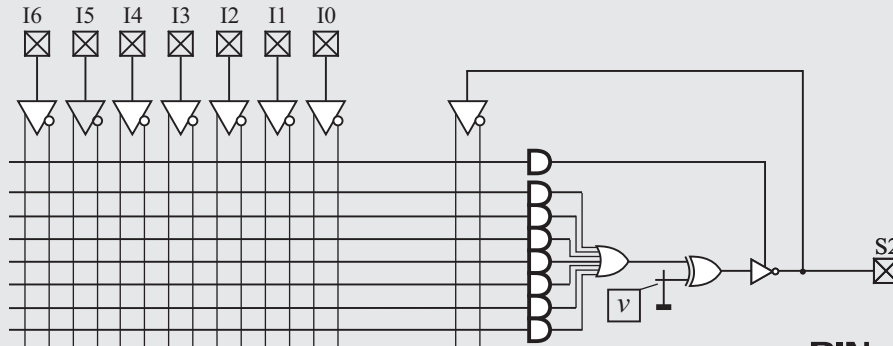


## Evolución de la lógica programable



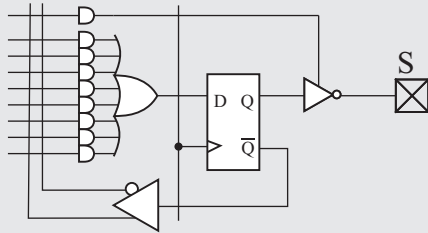
**Salidas negadas**

## Evolución de la lógica programable



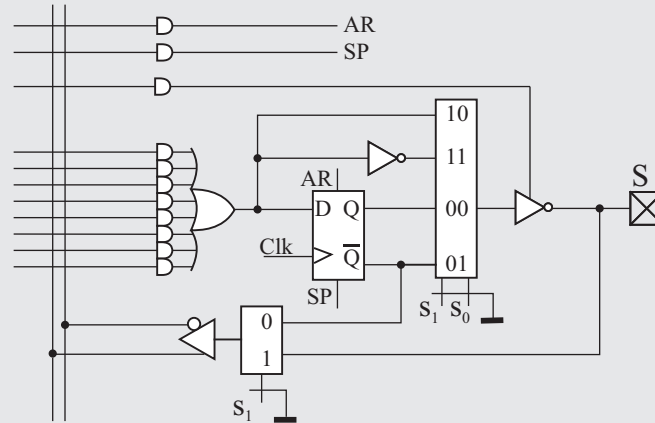
**PIN**

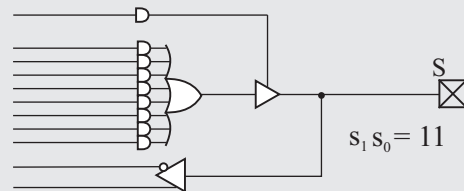
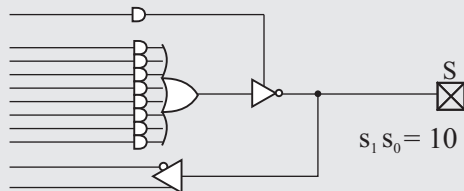
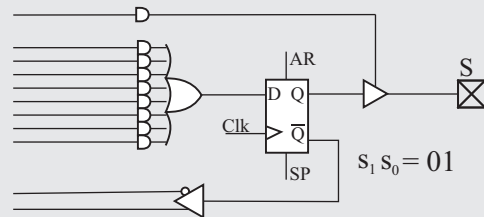
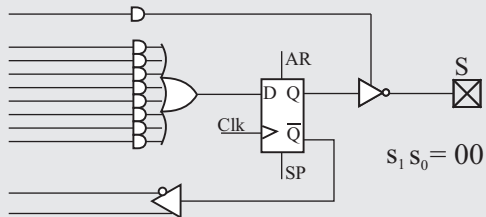
**Entrada / Salida**



## Evolución de la lógica programable

### Adición de Registros





**Configuraciones de salida para 22V10**