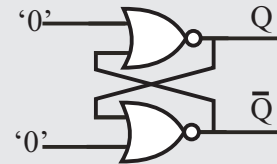
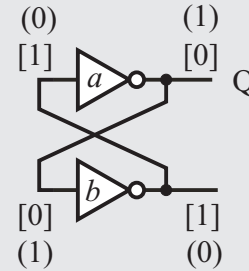
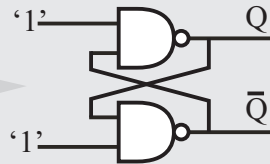
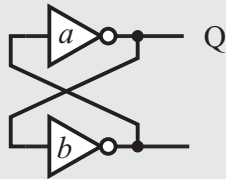
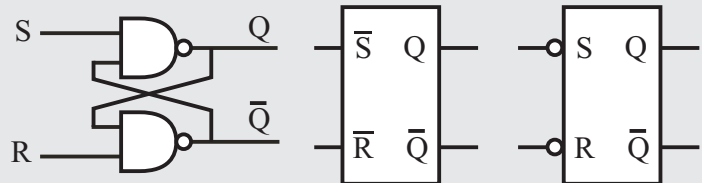
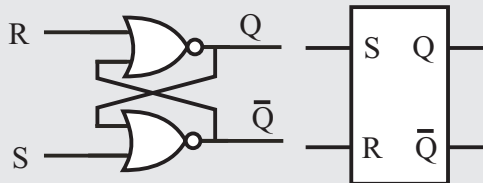


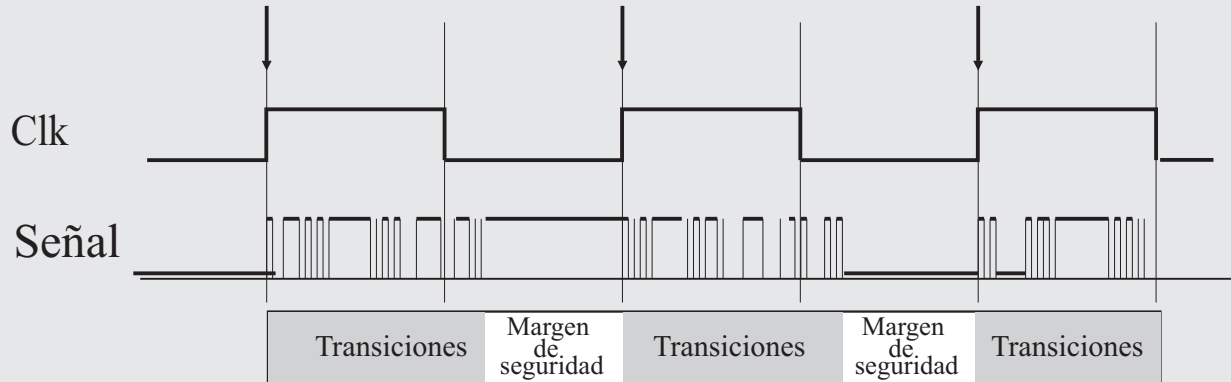
Biastable básico



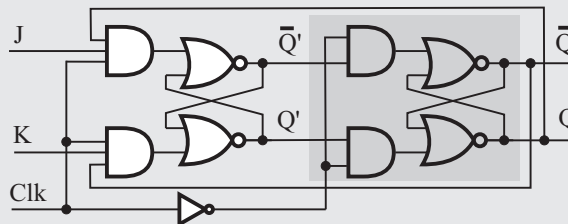
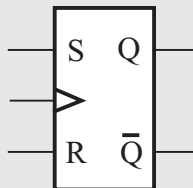
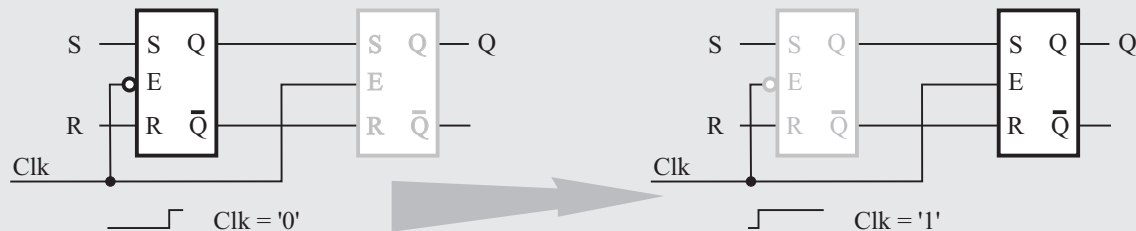
Biastables Set - Reset

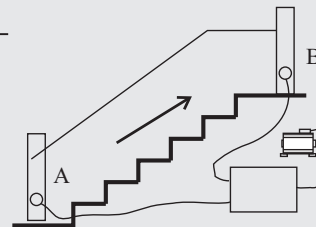
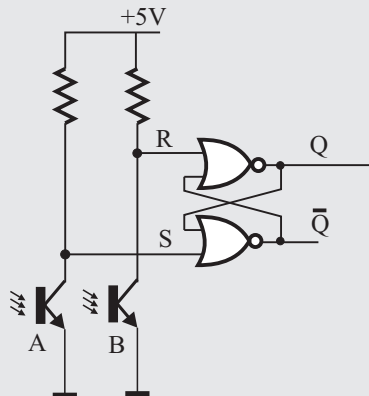
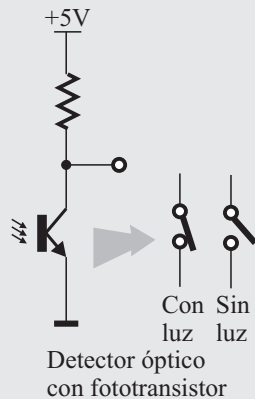
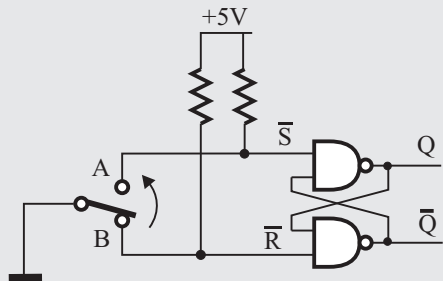


Transiciones tras el disparo síncrono de los biestables

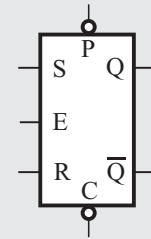
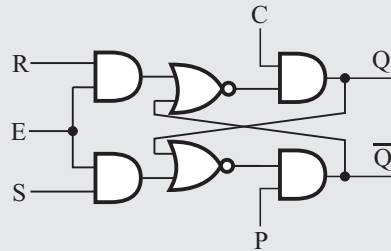
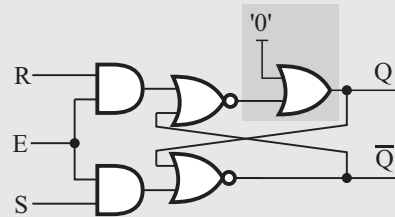


Estructura Maestro - Esclavo



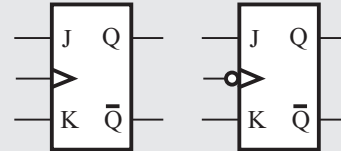


Ejemplos de utilización de biestables SR



Biestables síncronos con entradas asíncronas

Clk	J	K	$Q(t+1)$	
0	0		$Q(t)$	Reposo
0	1		0	Reset
1	0		1	Set
1	1		$\neg Q(t)$	Cambio de estado



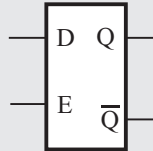
$Q(t)$	$Q(t+1)$	S	R
0	0	0	x
0	1	1	0
1	0	0	1
1	1	x	0

J	K	Q	$Q(t+1)$
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

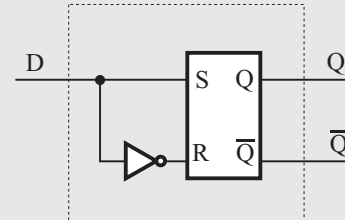
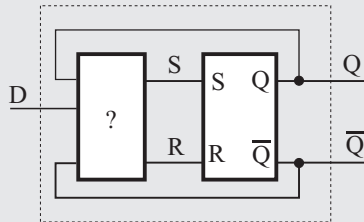
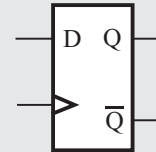
S	R
0	x
x	0
0	x
0	1
1	0
x	0
1	0
0	1

Biastable JK

E	D	$Q(t+1)$
0	0	$Q(t)$
0	1	$Q(t)$
1	0	0
1	1	1

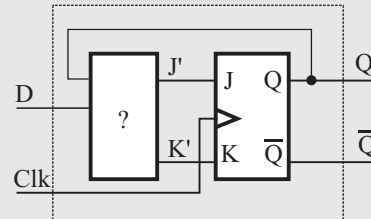
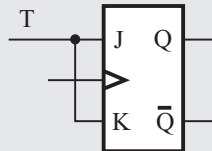
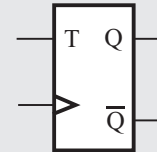


Clk	D	$Q(t+1)$
\uparrow	0	0
\uparrow	1	1



Bistable D y ejemplo de implementación

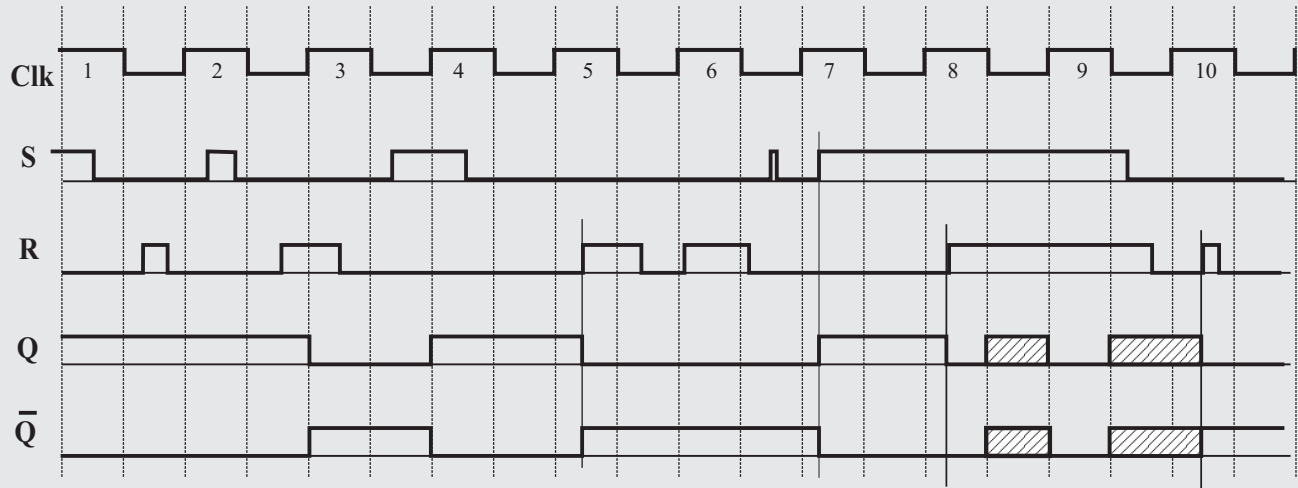
T	Q	Q(t+1)	Q(t)	Q(t+1)	T
0	0	0	0	0	0
0	1	1	0	1	1
1	0	1	1	0	1
1	1	0	1	1	0

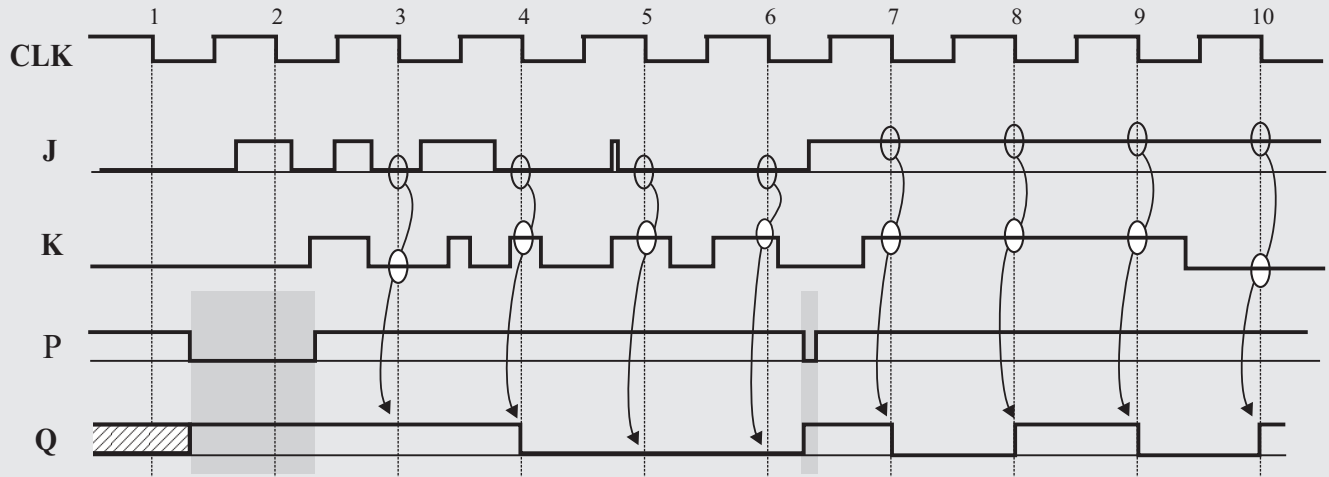


Bistable T y ejemplo de implementación



Cronograma del biestable SR





Ejemplo de cronograma con biestable JK

