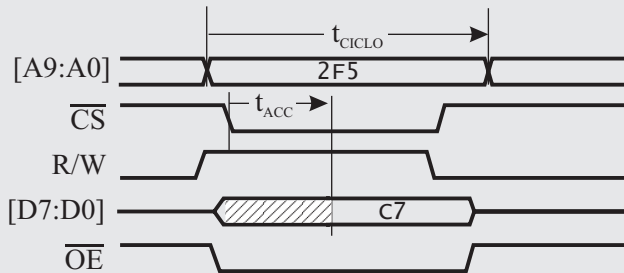
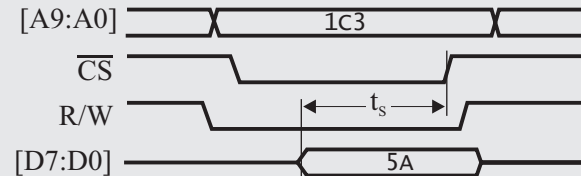
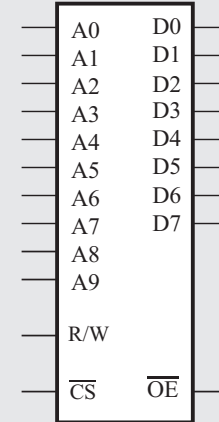


## Símbolo de una memoria y ciclos de lectura y escritura

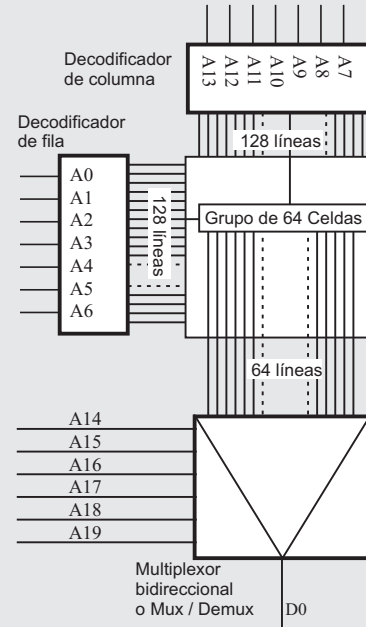
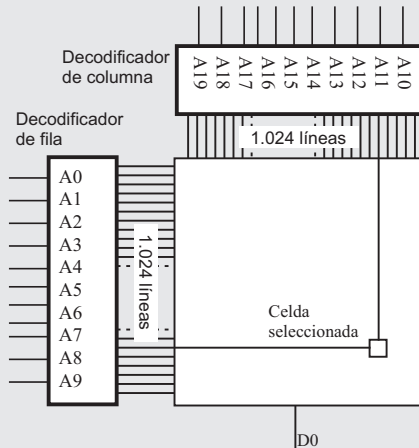
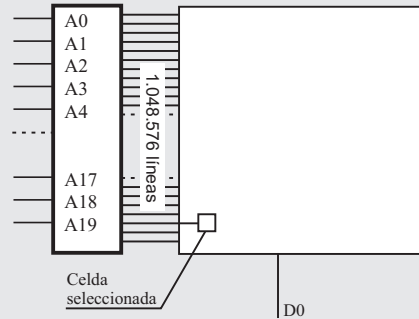


*Ciclo de lectura*

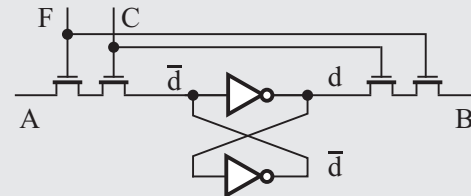
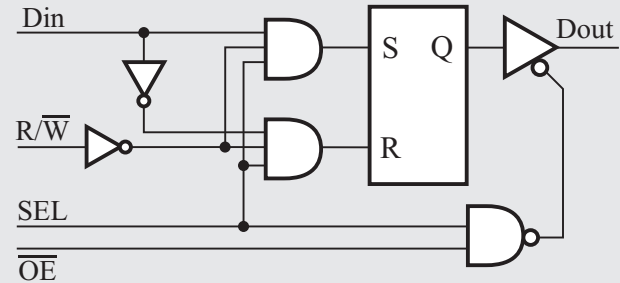
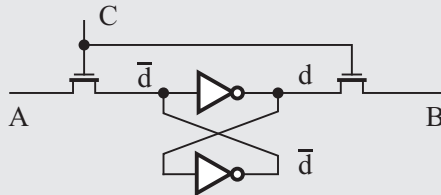
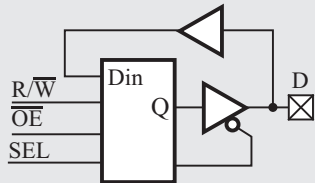


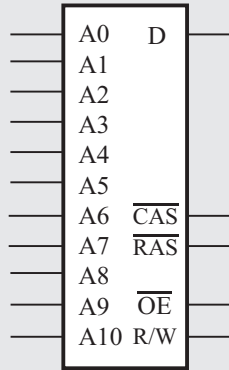
*Ciclo de escritura*

## Estructuras internas de una memoria

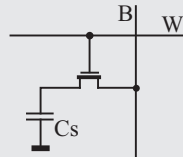


## Estructuras internas de una memoria

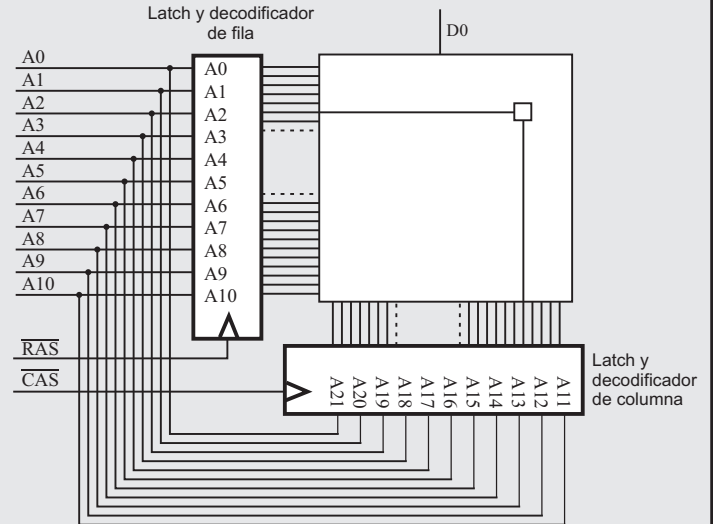


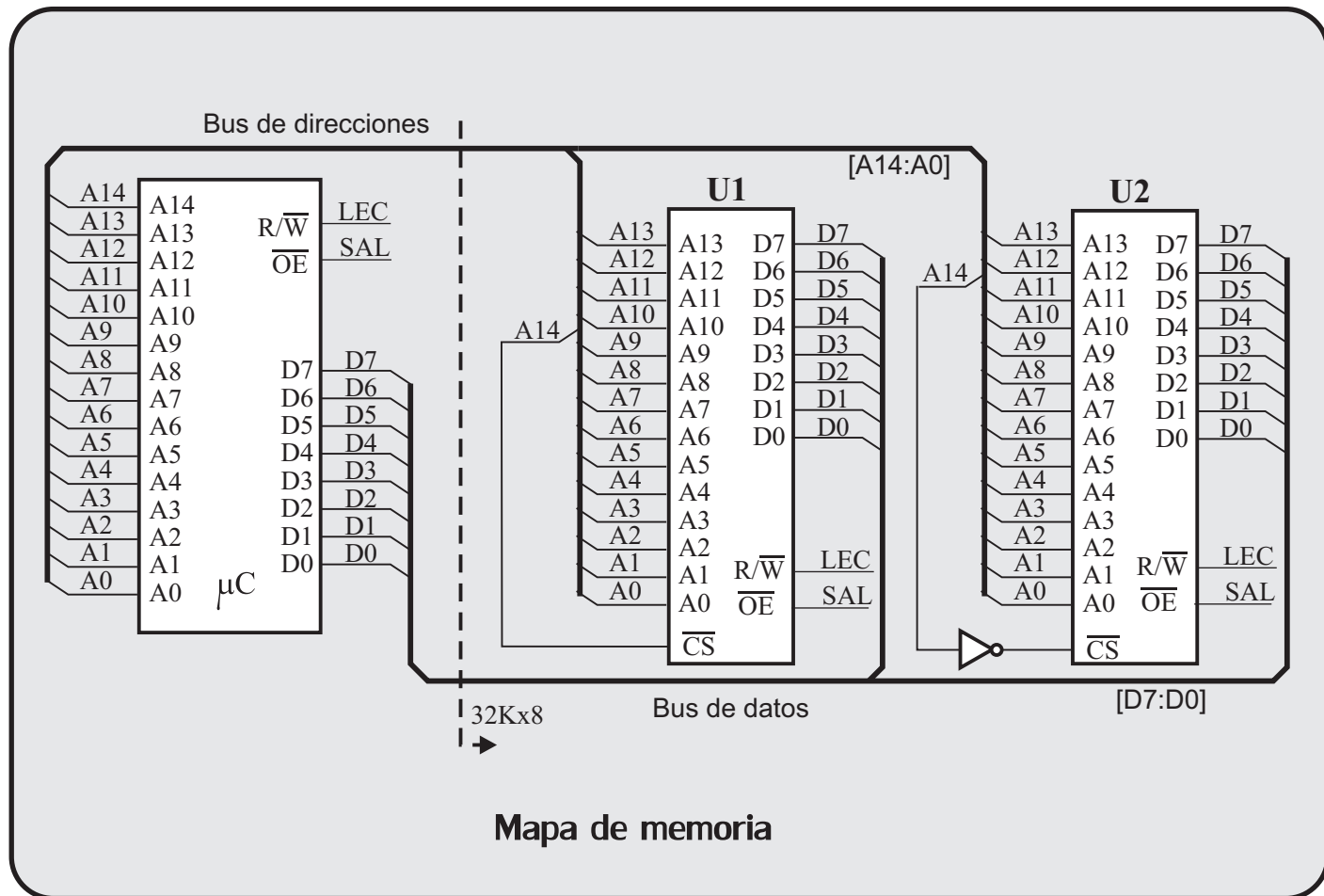


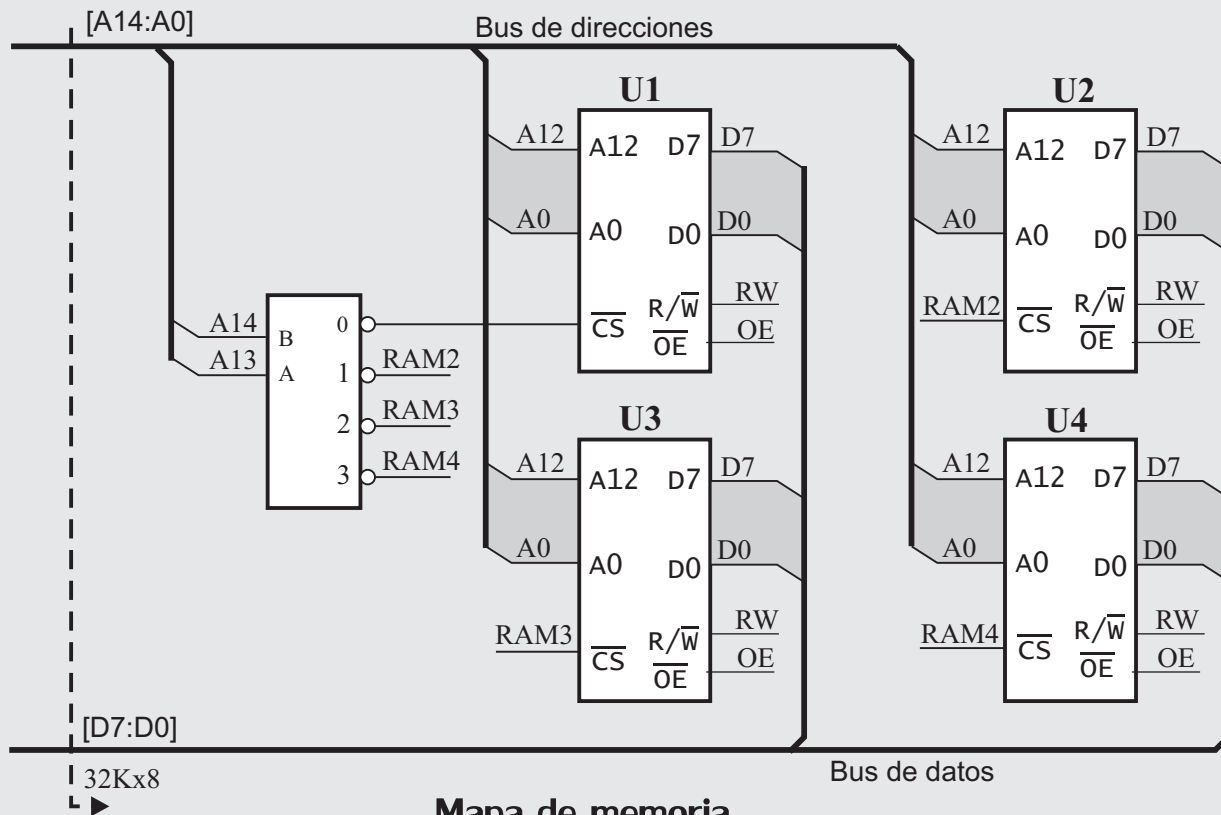
Memoria dinámica  
de 4M x 1

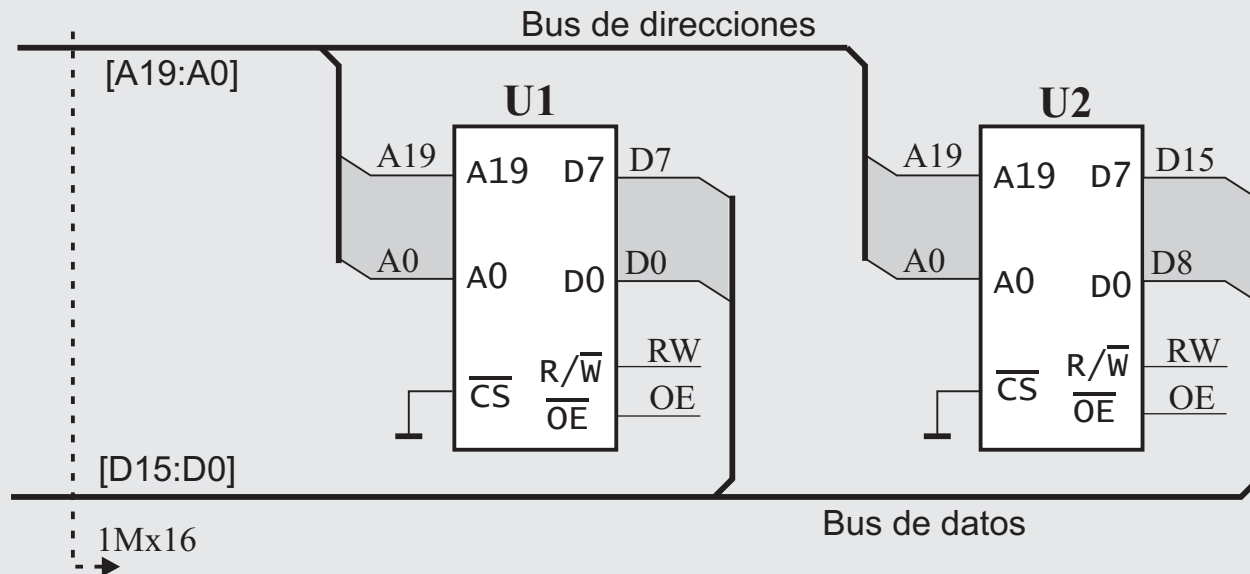


## Memoria Dinámica



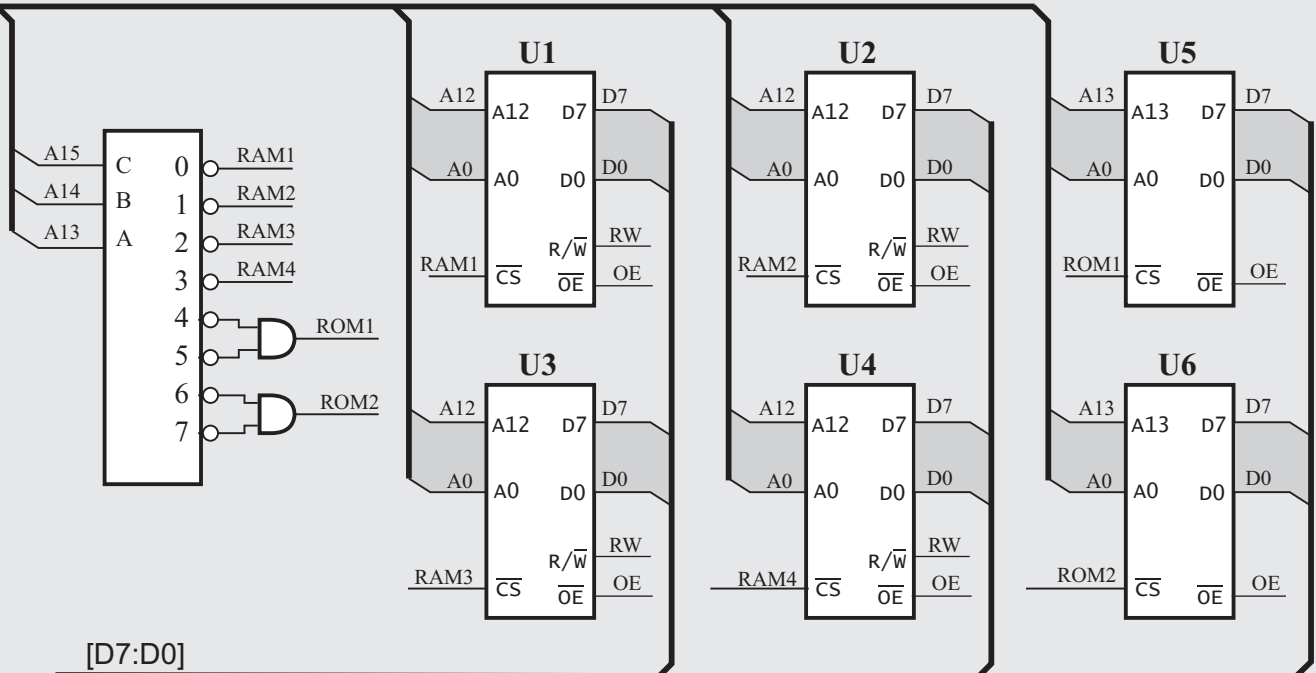






**Ampliación del Bus de Datos**

[A15:A0]



Decodificación de direcciones