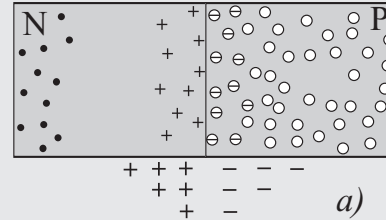
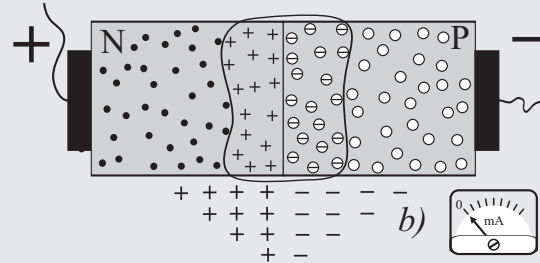


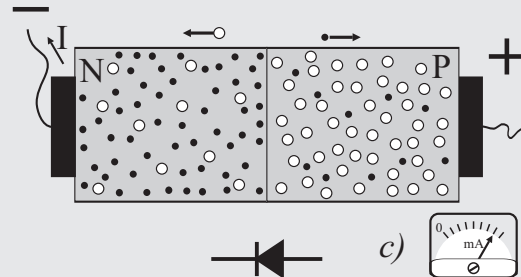
Unión PN: el DIODO



Polarización inversa

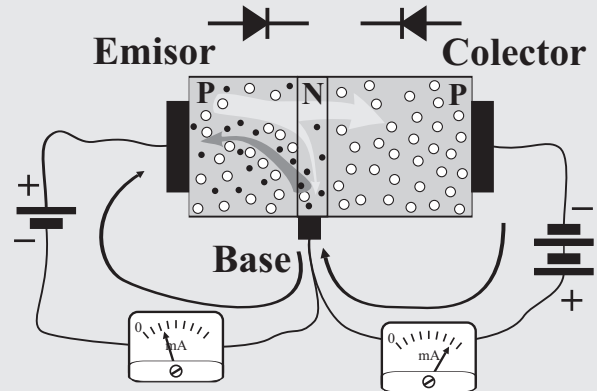
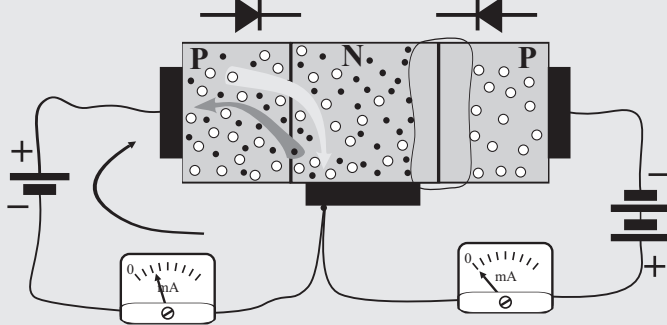


Polarización directa

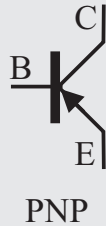


Transistor Bipolar

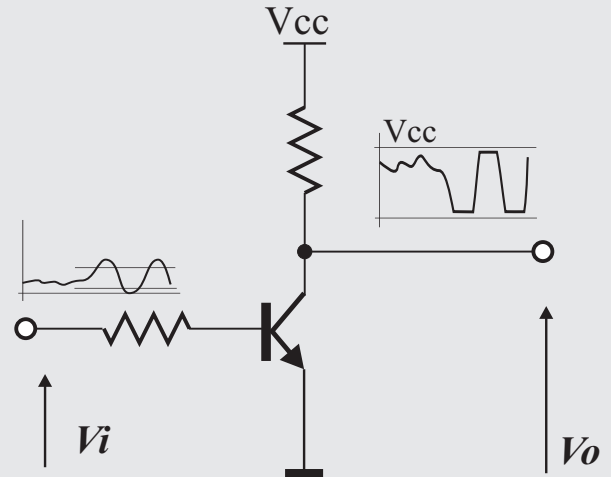
¡¡Esto no es un transistor!!



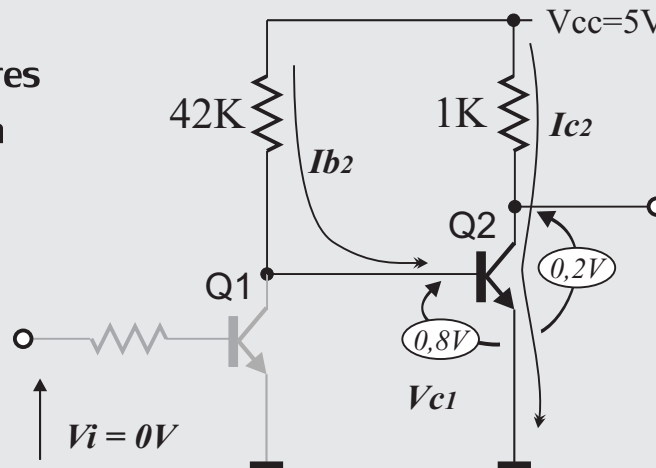
Símbolos del Transistor Bipolar



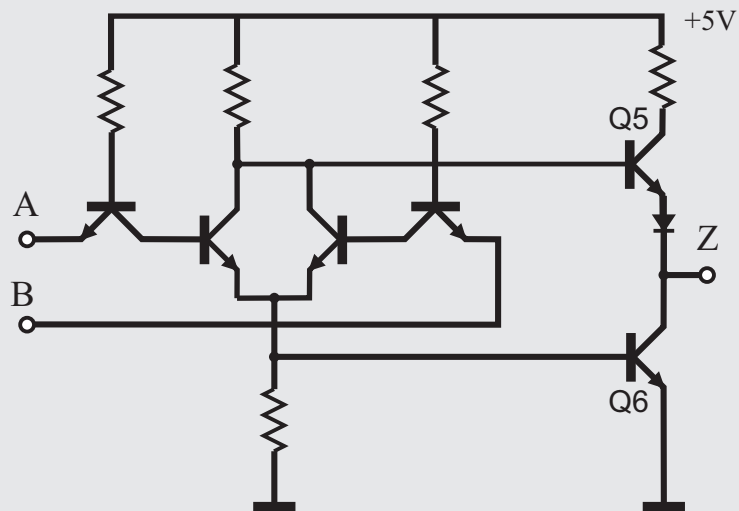
**NPN como amplificador
en Emisor común**



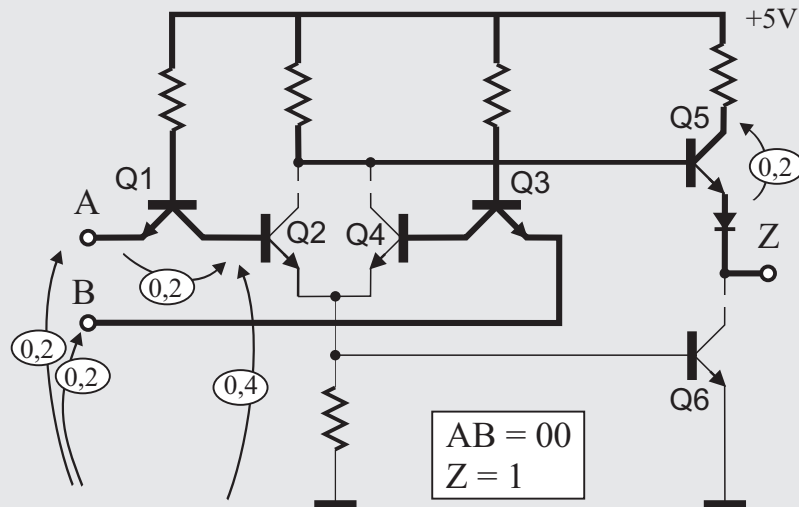
**Circuito con transistores
en Corte - Saturación**



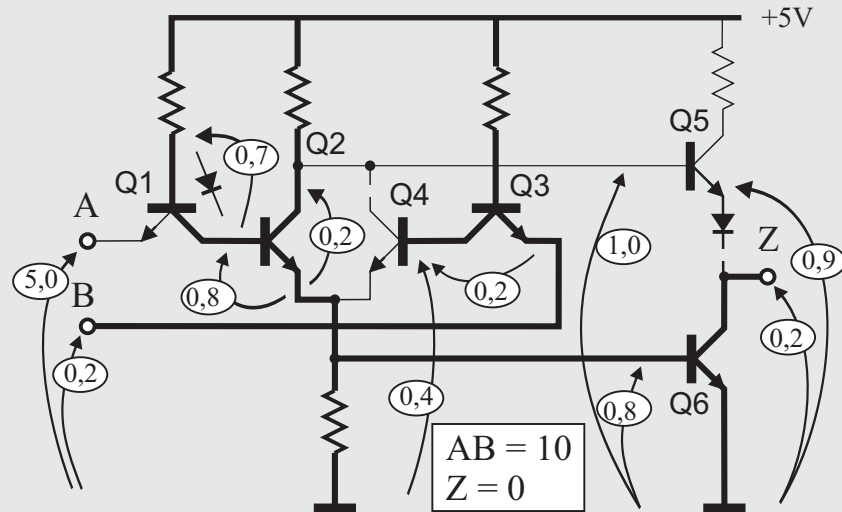
Puerta lógica TTL



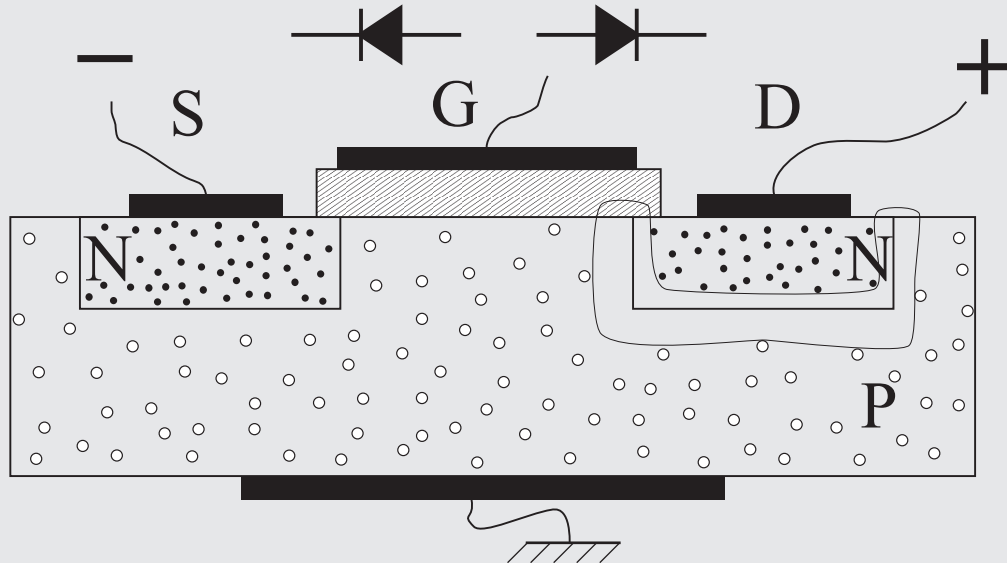
Puerta lógica TTL



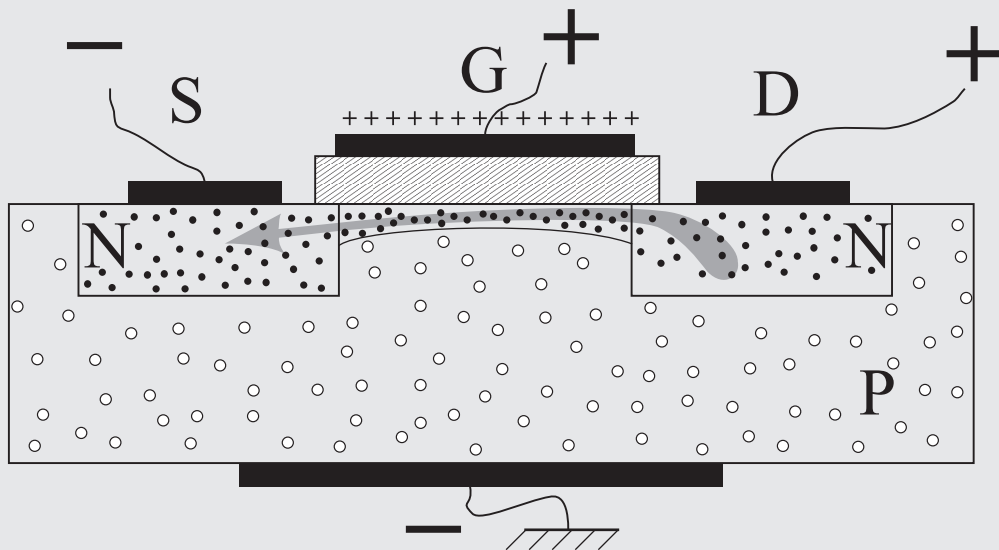
Puerta lógica TTL



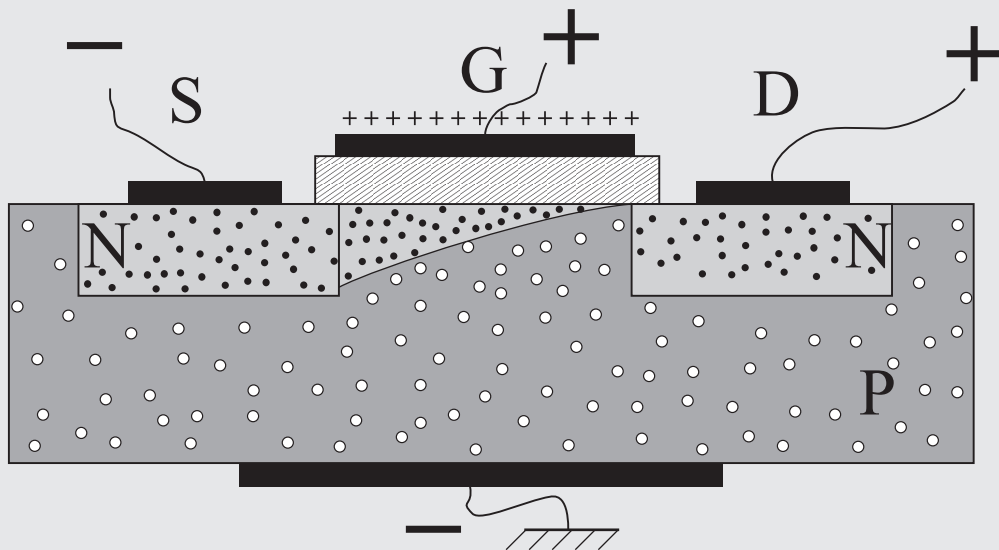
Transistor MOS



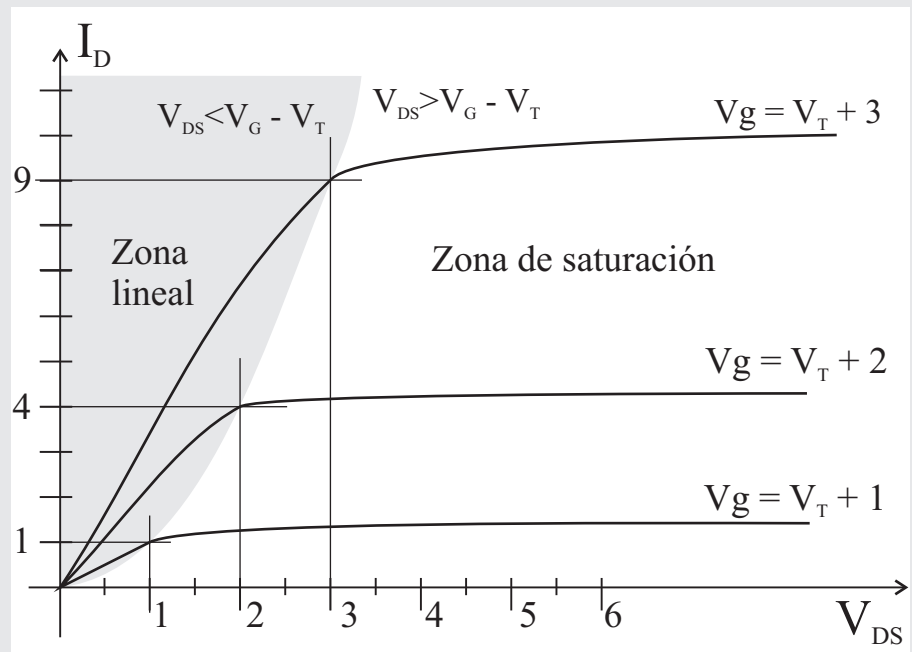
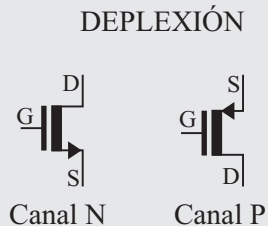
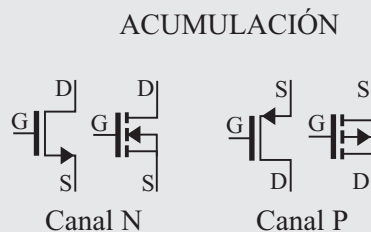
Transistor MOS



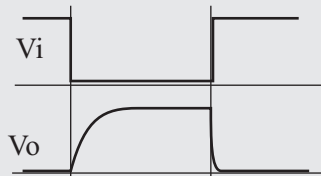
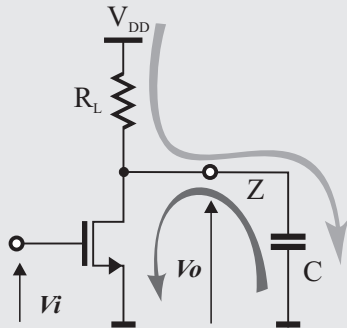
Transistor MOS



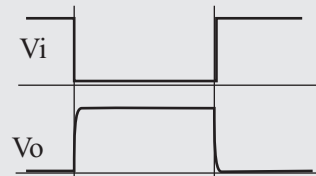
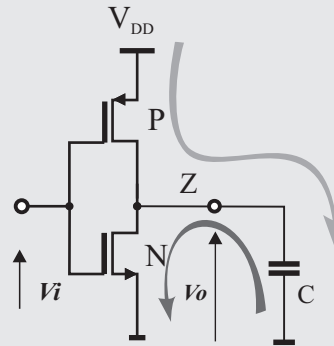
Transistor MOS



Comparación de los tiempos de conmutación



NMOS



CMOS