```
LDI R16, 0<<FOC0|1<<CS02|0<<CS01|1<<CS00|0<<COM01|1<<COM00|0<<WGM01|0<<WGM00
OUT TCCR0,R16
```

Bit	7	6	5	4	3	2	1	0	
	FOC0	WGM00	COM01	COM00	WGM01	CS02	CS01	CS00	**TCCR0**
	W	R/W	R/W	R/W	R/W	R/W	R/W	R/W	
Valor inicial	0	0	0	0	0	0	0	0	

Éste es el registro del OCR0:

Bit	7	6	5	4	3	2	1	0	
		OCR0 [7:0]							**OCR0**
	R/W	R/W	R/W	R/W	R/W	R/W	R/W	R/W	
Valor inicial	0	0	0	0	0	0	0	0	

El programa queda de la siguiente forma:

```
.INCLUDE "M8515DEF.INC"
.CSEG
.ORG 0

LDI R16,LOW(RAMEND)
OUT SPL,R16
LDI R16,HIGH(RAMEND)
OUT SPH,R16

LDI R16,0b0000_0001
OUT DDRB,R16        ;OC0 PWM OUTPUT (PB0)

LDI R16,0<<FOC0|1<<CS02|0<<CS01|1<<CS00|0<<COM01|1<<COM00|0<<WGM01|0<<WGM00
```

```
OUT TCCR0,R16

LDI R16,1          ;R16 DEBE VALER CUALQUIER NUMERO A
                   ;EXCEPCION DE CERO
OUT OCR0,R16

CICLO: RJMP CICLO
```

```
LDI R16,0<<FOC0|0<<CS02|0<<CS01|1<<CS00|0<<COM01|1<<COM00|0<<WGM01|0<<WGM00
OUT TCCR0,R16
```

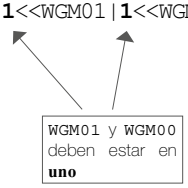
WGM01 y WGM00 deben estar en **cero**

```
LDI R16,0<<FOC0|1<<CS02|0<<CS01|1<<CS00|1<<COM01|0<<COM00|1<<WGM01|1<<WGM00
OUT TCCR0,R16

LDI R16,0          ;VALOR INICIAL

CICLO:
OUT OCR0,R16
INC R16
RCALL DELAY_1_SEGUNDO
RJMP CICLO

DELAY_1_SEGUNDO:
.
.
.
RET
```



```
LDI R16,255        ;VALOR INICIAL

CICLO:
OUT OCR0,R16
DEC R16
RCALL DELAY_1_SEGUNDO
RJMP CICLO
```

```
.INCLUDE "M8515DEF.INC"
.CSEG
.ORG 0

LDI R16,LOW(RAMEND)
OUT SPL,R16
LDI R16,HIGH(RAMEND)
OUT SPH,R16

LDI R16,0b0000_0001
OUT DDRB,R16            ;OC0 PWM OUTPUT (PB0)
```

```
LDI R16,0<<FOC0|1<<CS02|0<<CS01|1<<CS00|1<<COM01|0<<COM00|1<<WGM01|1<<WGM00
;             ------------ --------- --------------- ---------------
;                    SELECTOR          Clear OC0 on       FAST PWM
;                  DE FRECUENCIA       Compare Match
;                                      set OC0 at TOP
OUT TCCR0,R16

CICLO:
LDI R16,1
OUT OCR0,R16
RCALL DELAY_1_SEGUNDO

LDI R16,2
OUT OCR0,R16
RCALL DELAY_1_SEGUNDO

LDI R16,3
OUT OCR0,R16
RCALL DELAY_1_SEGUNDO

LDI R16,4
OUT OCR0,R16
RCALL DELAY_1_SEGUNDO
.
.
.
LDI R16,8
OUT OCR0,R16
RCALL DELAY_1_SEGUNDO

LDI R16,9
OUT OCR0,R16
RCALL DELAY_1_SEGUNDO
RJMP CICLO

DELAY_1_SEGUNDO:
.
.
.
RET
```

Cada valor de OCR0 es una posición específica del servo-motor

```
LDI R16,1
LDI R17,9

CICLO:
CP R16,R17
BREQ FIN

OUT OCR0,R16
RCALL DELAY_2

INC R16

RJMP CICLO

FIN:
LDI R16,1
RJMP CICLO
```